L Number	Hits	Search Text	DB	Time stamp
-	1	memory near3 optimiz$ near3 tool	USPAT; US-PGPUB; IBM_TDB	2003/10/15 09:33
-	437	memory near3 design$ near3 applicatio$	USPAT; US-PGPUB; IBM_TDB	2003/10/15 09:35
-	146	(memory near3 design$ near3 applicatio$) and optim$	USPAT; US-PGPUB; IBM_TDB	2003/10/15 09:48
-	15	memory near2 circuit$ near3 compiler	USPAT; US-PGPUB; IBM_TDB	2003/10/15 09:50
-	196	memory near2 requir$ near3 optimiz$	USPAT; US-PGPUB; IBM_TDB	2003/10/15 09:52
-	75	(memory near2 requir$ near3 optimiz$) and simulat$	USPAT; US-PGPUB; IBM_TDB	2003/10/15 10:02
-	1084	memory near2 emulat$	USPAT; US-PGPUB; IBM_TDB	2003/10/15 10:03
-	141	(memory near2 emulat$) and asic	USPAT; US-PGPUB; IBM_TDB	2003/10/15 10:20
-	14	(memory near2 emulat$) and asic and statist$	USPAT; US-PGPUB; IBM_TDB	2003/10/15 10:32
-	188029	memory and design	USPAT; US-PGPUB; IBM_TDB	2003/10/15 10:32
-	190207	(memory and design) emulator	USPAT; US-PGPUB; IBM_TDB	2003/10/15 10:33
-	9875	(memory and design) and emulat$	USPAT; US-PGPUB; IBM_TDB	2003/10/15 10:33
-	234	((memory and design) and emulat$) and flash and ram and rom and dram and eeprom	USPAT; US-PGPUB; IBM_TDB	2003/10/15 10:35
-	234	(((memory and design) and emulat$) and flash and ram and rom and dram and eeprom) and design	USPAT; US-PGPUB; IBM_TDB	2003/10/15 10:36
-	120	((((memory and design) and emulat$) and flash and ram and rom and dram and eeprom) and design) and optimi$	USPAT; US-PGPUB; IBM_TDB	2003/10/15 10:36
-	118	(((((memory and design) and emulat$) and flash and ram and rom and dram and eeprom) and design) and optimi$) and circuit	USPAT; US-PGPUB; IBM_TDB	2003/10/15 10:55
-	1	"5822564".PN.	USPAT; US-PGPUB	2003/10/15 10:51
-	1	"5812414".PN.	USPAT; US-PGPUB	2003/10/15 10:52
-	1	"5796623".PN.	USPAT; US-PGPUB	2003/10/15 10:54
-	1	"5146460".PN.	USPAT; US-PGPUB	2003/10/15 10:54
-	38	memory near design near application$	USPAT; US-PGPUB; IBM_TDB	2003/10/15 10:59
-	1	"5848017".PN.	USPAT	2003/10/15 10:58
-	1	"5848017".PN.	USPAT	2003/10/15 10:58
-	1	"5740098".PN.	USPAT	2003/10/15 10:58
-	1	"5726994".PN.	USPAT	2003/10/15 10:58
-	317	memory near3 specifi$ near3 design	USPAT; US-PGPUB; IBM_TDB	2003/10/15 11:03
-	143	(memory near3 specifi$ near3 design) and controller	USPAT; US-PGPUB; IBM_TDB	2003/10/15 11:17

–	11	(memory near3 specifi$ near3 design) and statist$	USPAT; US-PGPUB; IBM_TDB	2003/10/15 11:30
–	251	asic same memory near3 design$	USPAT; US-PGPUB; IBM_TDB	2003/10/15 11:30

L Number	Hits	Search Text	DB	Time stamp
-	0	("09745561").pn.	USPAT	2003/10/15 13:12
-	1	0083112.pn.	USPAT	2003/10/15 13:11
-	0	09745561.pn.	USPAT	2003/10/15 13:12
-	0	09745561.an.	USPAT	2003/10/15 13:12
-	0	20020083112.pn.	USPAT	2003/10/15 13:15
-	2	20020083112.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 13:15
-	0	("asicsame(RAMnear5ROMnear5FLASHnear5EEPROM)")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/22 08:22
-	0	("asicsame(RAMnear5ROMnear5FLASHnear5EEPROM)")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/22 08:22
-	0	("ASICSAME(RAMnear5ROMnear5FLASHnear5EEPROM)")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/22 08:22
-	0	("ASIC(RAMnear5ROMnear5FLASHnear5EEPROM)").PN	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/22 09:31
-	0	("(9491390).AN.").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 10:37
-	0	("(9591621).AN.").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 10:37

Most Frequently Occurring Classifications of Patents Returned
From A Search of 09591621 on October 09, 2003

Original Classifications
   3   365/230.03
   3   382/232
   3   382/248
   2   710/100

Cross-Reference Classifications
   3   382/233
   3. 382/240
   3   705/53
   2   257/E27.097
   2   360/133
   2   382/248
   2   710/104
   2   714/718

Combined Classifications
   5   382/248
   4   382/240
   3   365/230.03
   3   382/232
   3   382/233
   3   705/53
   2   257/E27.097
   2   360/133
   2   365/230.01
   2   369/290
   2   705/57
   2   705/80
   2   710/100
   2   710/104
   2   714/718
   2   714/773

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09591621_CLSTITLES
Titles of Most Frequently Occurring Classifications of Patents Returned
From A Search of 09591621 on October 09, 2003

```
5   382/248        (3 OR, 2 XR)
         Class   382 :  IMAGE ANALYSIS
         382/232        IMAGE COMPRESSION OR CODING
         382/248        .Transform coding

4   382/240        (1 OR, 3 XR)
         Class   382 :  IMAGE ANALYSIS
         382/232        IMAGE COMPRESSION OR CODING
         382/240        .Pyramid, hierarchy, or tree structure

3   365/230.03     (3 OR, 0 XR)
         Class   365 :  STATIC INFORMATION STORAGE AND RETRIEVAL
         365/230.01     ADDRESSING
         365/230.03     .Plural blocks or banks

3   382/232        (3 OR, 0 XR)
         Class   382 :  IMAGE ANALYSIS
         382/232        IMAGE COMPRESSION OR CODING

3   382/233        (0 OR, 3 XR)
         Class   382 :  IMAGE ANALYSIS
         382/232        IMAGE COMPRESSION OR CODING
         382/233        .Including details of decompression

3   705/53         (0 OR, 3 XR)
         Class   705 :  DATA PROCESSING: FINANCIAL, BUSINESS
                        PRACTICE, MANAGEMENT, OR COST/PRICE DETERMIN
ATION
         705/50         BUSINESS PROCESSING USING CRYPTOGRAPHY
         705/51         .Usage protection of distributed data files
         705/52         ..Usage or charge determination
         705/53         ...Including third party for collecting or
                        distributing payment (e.g., clearinghouse)

2   257/E27.097    (0 OR, 2 XR)
         Class   257 :  ACTIVE SOLID-STATE DEVICES
         257/E27.006    .Including piezo-electric, electro-resistive,
                        or magneto-resistive component (EPO)
         257/E27.009    .Including semiconductor component with at
                        least one potential barrier or surface
barrier adapted for
                        rectifying, oscillating, amplifying, o
r switching, or
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Including integrated passive circuit e
lements (EPO)
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        257/E27.01     ..With semiconductor substrate only (EPO)
        257/E27.07     ...Including a plurality of individual
                          components in a repetitive configuration
(EPO)
        257/E27.081    ....Including field-effect component (EPO)
        257/E27.084    .....Dynamic random access memory, DRAM,
                          structure (EPO)
        257/E27.097    ......Peripheral structure (EPO)


  2  360/133        (0 OR, 2 XR)
        Class   360 :  DYNAMIC MAGNETIC INFORMATION STORAGE OR
                          RETRIEVAL
        360/131         RECORD MEDIUM
        360/132         .In container
        360/133         ..For disk


  2  365/230.01     (1 OR, 1 XR)
        Class   365 :  STATIC INFORMATION STORAGE AND RETRIEVAL
        365/230.01      ADDRESSING


  2  369/290        (1 OR, 1 XR)
        Class   369 :  DYNAMIC INFORMATION STORAGE OR RETRIEVAL
        369/272         STORAGE MEDIUM STRUCTURE
        369/289         .Adjuncts or adapters
        369/290         ..For central area of disc (e.g., hole size or
                          drive sticker)


  2  705/57         (1 OR, 1 XR)
        Class   705 :  DATA PROCESSING: FINANCIAL, BUSINESS
                          PRACTICE, MANAGEMENT, OR COST/PRICE DETERMIN
ATION
        705/50          BUSINESS PROCESSING USING CRYPTOGRAPHY
        705/51          .Usage protection of distributed data files
        705/57          ..Copy protection or prevention


  2  705/80         (1 OR, 1 XR)
        Class   705 :  DATA PROCESSING: FINANCIAL, BUSINESS
                          PRACTICE, MANAGEMENT, OR COST/PRICE DETERMIN
ATION
        705/80          ELECTRONIC NEGOTIATION


  2  710/100        (2 OR, 0 XR)
        Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                          PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/100         INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                          TRANSACTION PROCESSING)
```

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2  710/104        (0 OR, 2 XR)
        Class   710 :   ELECTRICAL COMPUTERS AND DIGITAL DATA
                            PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/100           INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                            TRANSACTION PROCESSING)
        710/104           .System configuring


2  714/718        (0 OR, 2 XR)
        Class   714 :   ERROR DETECTION/CORRECTION AND FAULT
                            DETECTION/RECOVERY
        714/699           PULSE OR DATA ERROR HANDLING
        714/718           .Memory testing


2  714/773        (1 OR, 1 XR)
        Class   714 :   ERROR DETECTION/CORRECTION AND FAULT
                            DETECTION/RECOVERY
        714/699           PULSE OR DATA ERROR HANDLING
        714/746           .Digital data error correction
        714/752           ..Forward correction by block code
        714/763           ...Memory access
        714/773           ....Solid state memory
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PLUS Search Results for S/N 09591621, Searched October 09, 2003

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to 35
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